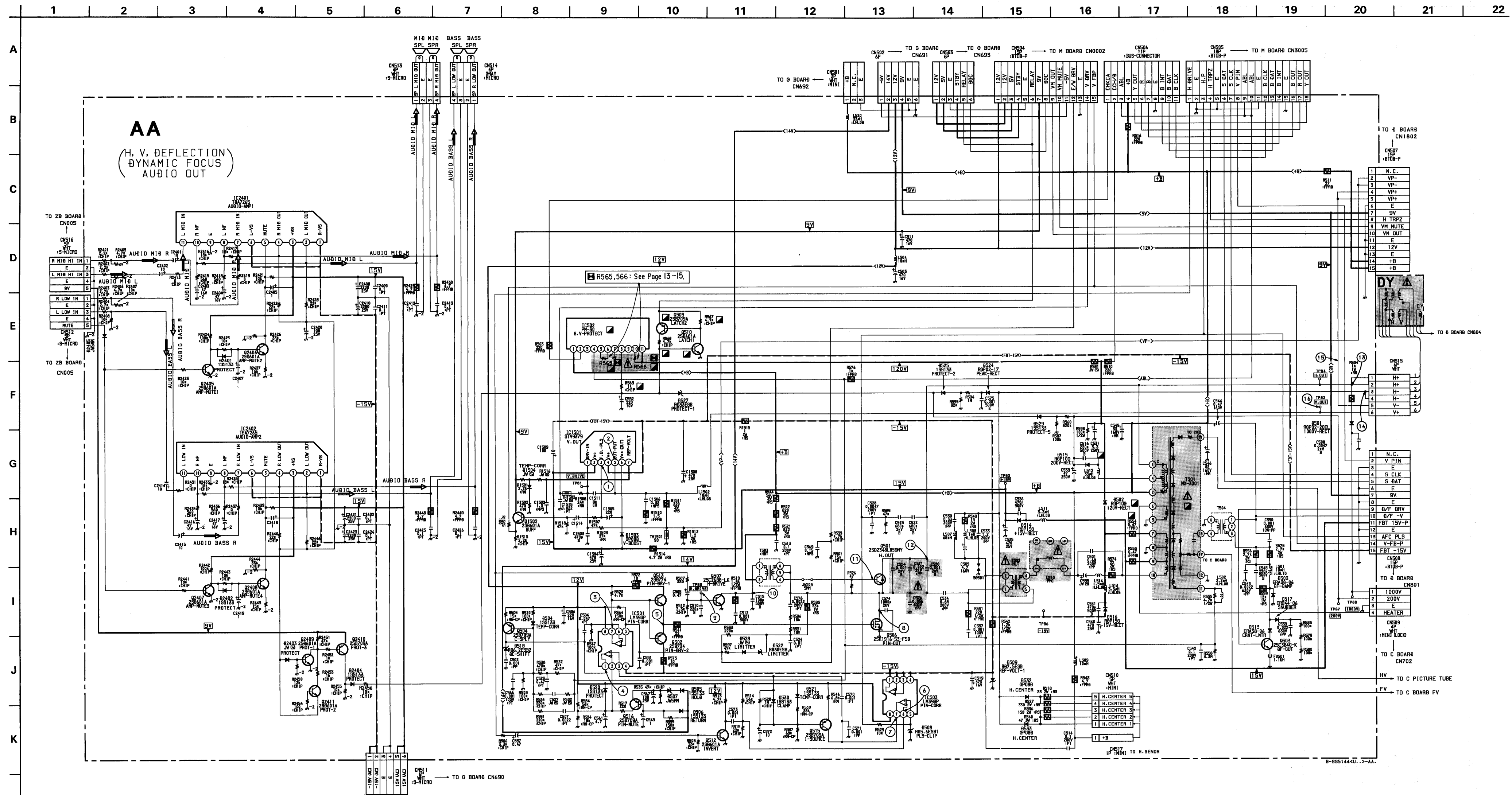


① 35.4 Vp-p (V)	② 63.8 Vp-p (V)	③ 37.4 Vp-p (H)
④ 11.5 Vp-p (H)	⑤ 18.5 Vp-p (H)	⑥ 16.4 Vp-p (H)
⑦ 10.1 Vp-p (H)	⑧ 176 Vp-p (H)	⑨ 3.2 Vp-p (H)
⑩ 128 Vp-p (H)	⑪ 176 Vp-p (H)	⑫ 1010 Vp-p (H)
⑬ 1010 Vp-p (H)	⑭ 176 Vp-p (H)	⑮ 113 Vp-p (V)
⑯ 64.2 Vp-p (V)		



IC501	① - 5.2	③ 0.6	⑤ 6.0	⑦ 8.3
	② 1.8	④ - 7.2	⑥ 0	⑧ 11.6
IC502	① 136	④ 1.8	⑦ GND	⑩ NC
	② 136	⑤ 128	⑧ 0	⑪ 138
	③ NC	⑥ 53.7	⑨ 134	
IC503	① - 7.2	③ - 7.2	⑤ 0.3	⑦ - 5.4
	② - 7.2	④ - 7.2	⑥ 3.3	⑧ 11.6
IC1501	① 1.6	③ - 14.5	⑤ 0.4	⑦ 1.8
	② 15.7	④ - 17.3	⑥ 16.7	
IC2401	① - 16.8	④ 0	⑦ 0	⑩ 0
	② 0	⑤ 9.2	⑧ 0	⑪ 0
	③ 17.2	⑥ - 16.9	⑨ GND	
IC2402	① - 16.8	④ 0	⑦ 0	⑩ 0
	② 0	⑤ 9.1	⑧ 0	⑪ 0
	③ 17.2	⑥ - 16.9	⑨ GND	

	E	C	B
Q501	18.7	138	18.6
Q502	8.3	- 7.2	8.3
Q503	GND	77.6	0.4
Q504	5.4	- 5.2	4.8
Q507	GND	67.6	- 1.0
Q509	4.5	0	4.5
Q510	GND	4.5	0
Q512	GND	6.4	- 1.2
Q513	8.3	11.6	8.3
Q515	8.2	- 5.4	7.5
Q516	15.8	1.8	16.0
Q11502	0.5	9.1	- 0.6
Q2401	GND	0	0
Q2402	GND	0	0
Q2403	GND	0	0
Q2404	GND	0	0
Q2405	GND	10.5	0
Q2406	9.2	9.2	9.8
Q2407	GND	10.4	0
Q2408	9.1	9.1	9.7
Q2409	GND	9.0	0
Q2410	9.1	0	9.0
Q2411	0	9.0	0
	S	D	G
Q506	GND	18.7	8.3

The block diagram illustrates the internal architecture of the TDA7267. It features a central **POWER AMPLIFIER** block with an **INVERTING INPUT** (pin 1) and a **NON-INVERTING INPUT** (pin 7). The output of the power amplifier (pin 5) is connected to a **FLYBACK GENERATOR** block, which in turn provides the **OUTPUT STAGE SUPPLY** (pin 6) and the **FLYBACK GENERATOR** output (pin 3). A **THERMAL PROTECTION** block is also shown, connected to the power amplifier's output. The circuit is powered by a **SUPPLY VOLTAGE** (pin 2) and grounded at pin 4.

[illegible]